



FIG. 1A

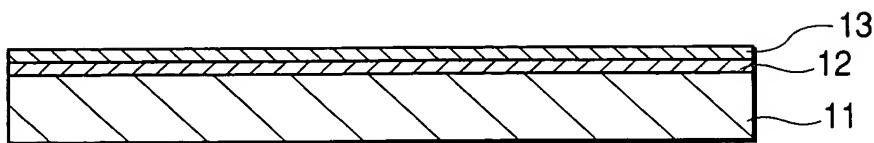


FIG. 1B

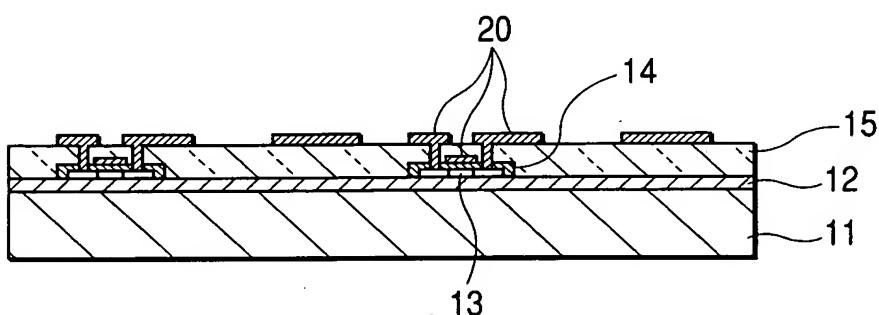


FIG. 1C

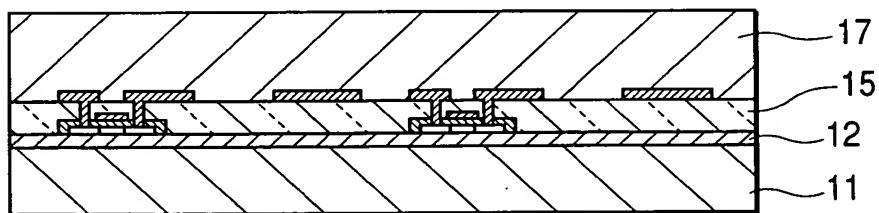


FIG. 1D

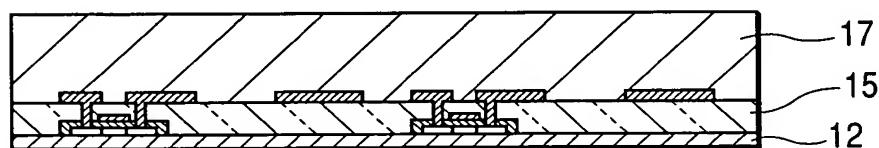


FIG. 1E

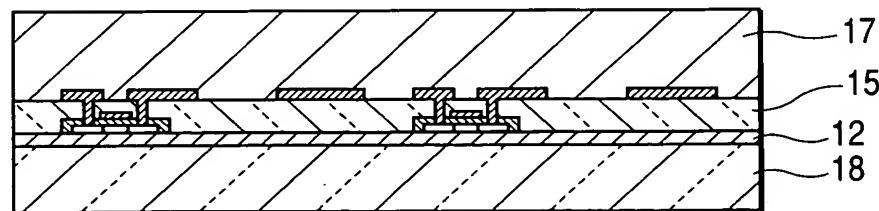


FIG. 1F

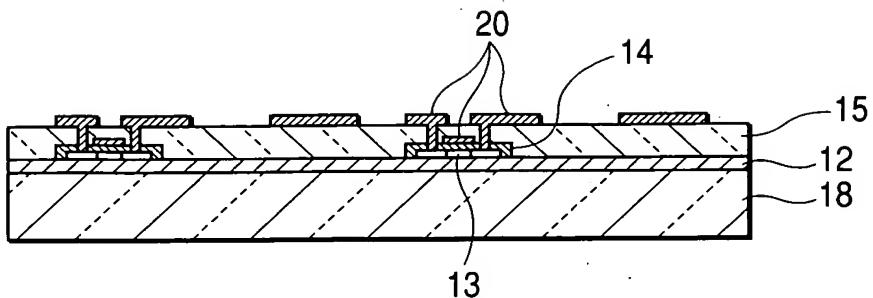


FIG. 1G

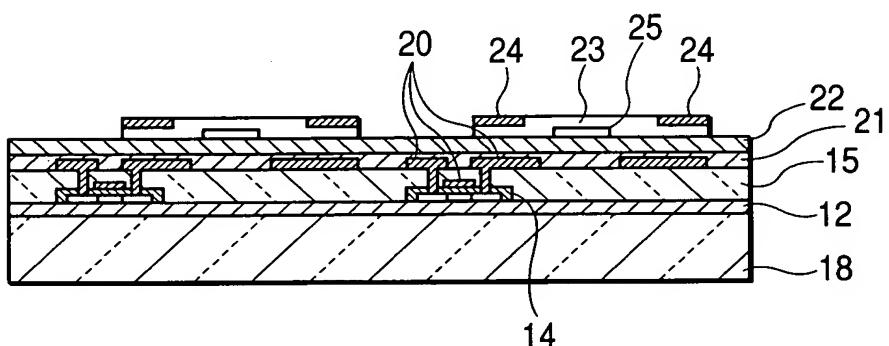


FIG. 1H

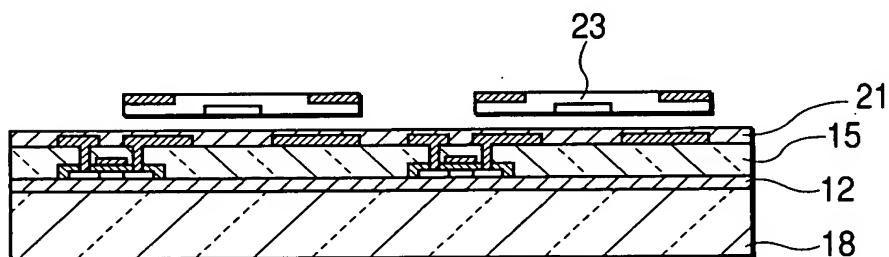


FIG. 1I

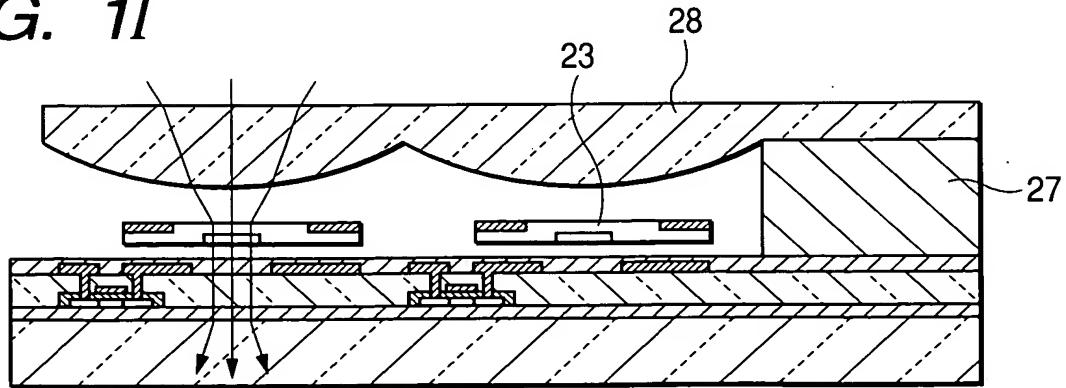


FIG. 2A

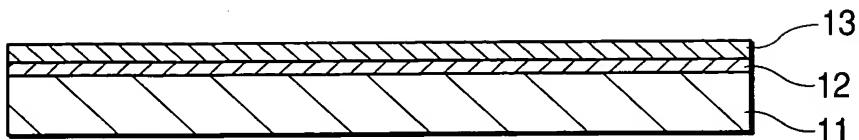


FIG. 2B

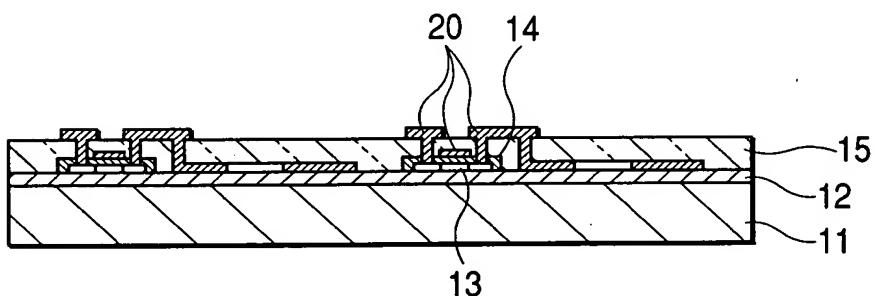


FIG. 2C

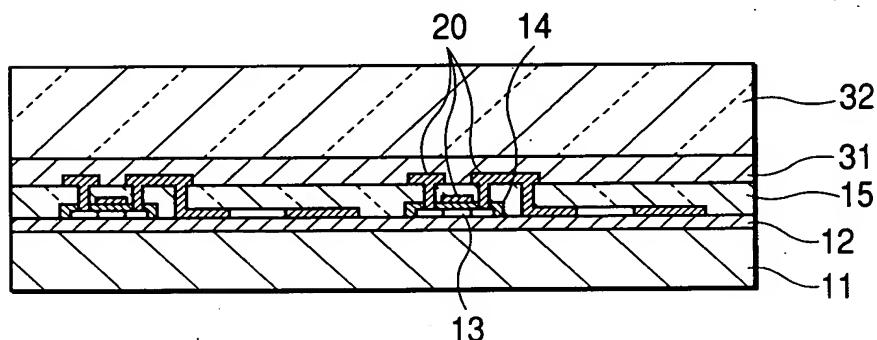


FIG. 2D

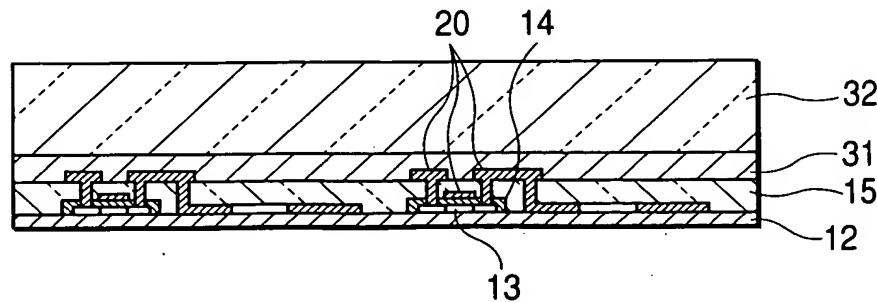


FIG. 2E

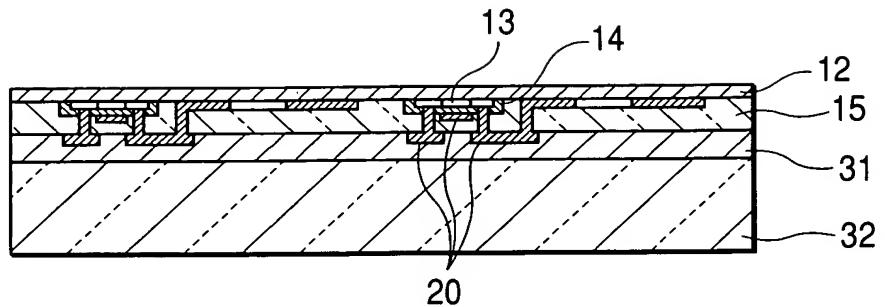


FIG. 2F

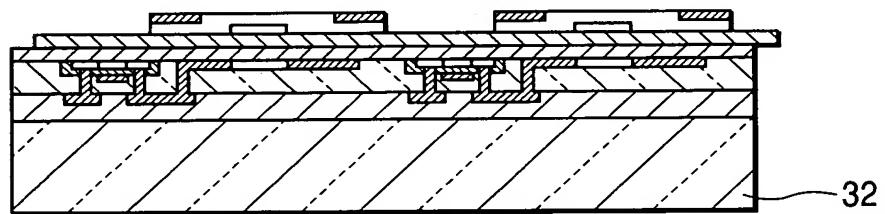


FIG. 2G

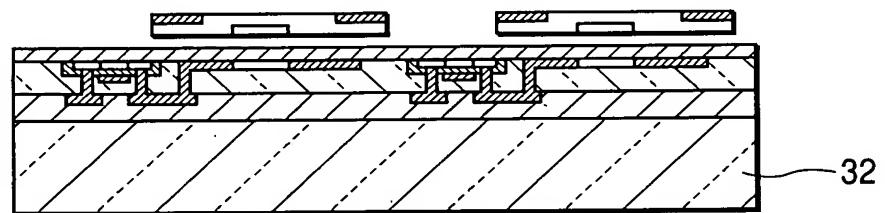


FIG. 2H

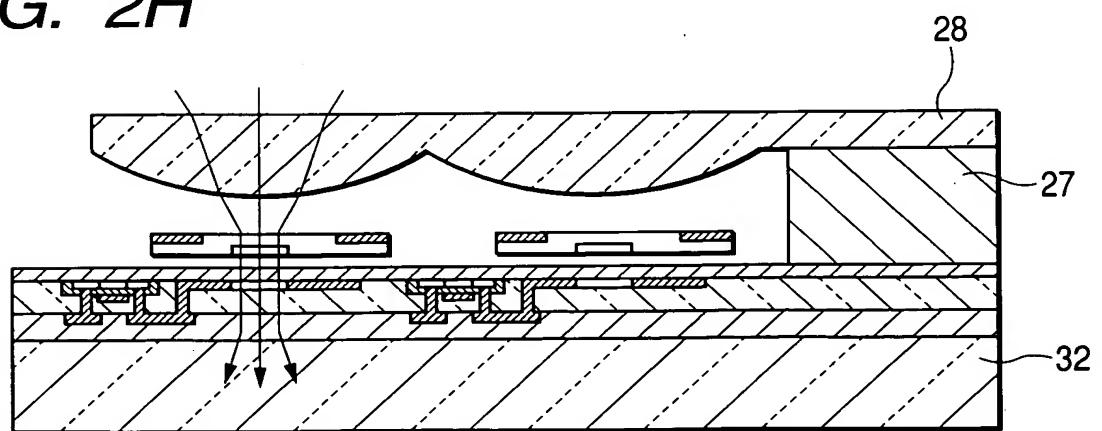


FIG. 3A



FIG. 3B

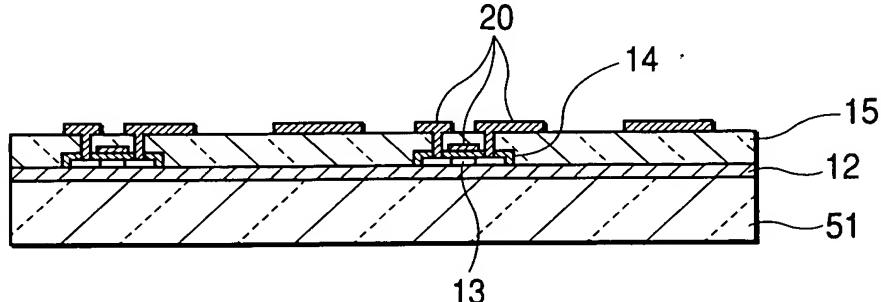


FIG. 3C

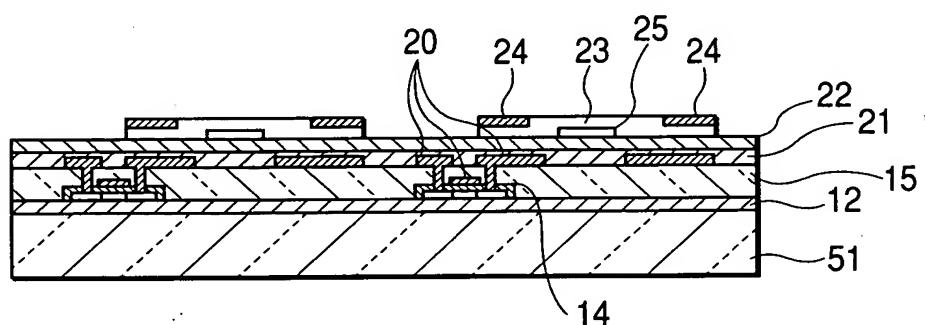


FIG. 3D

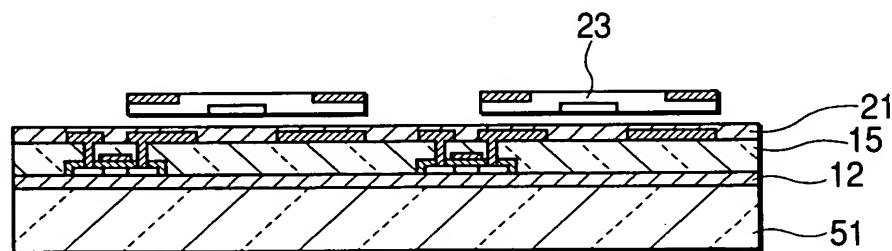


FIG. 3E

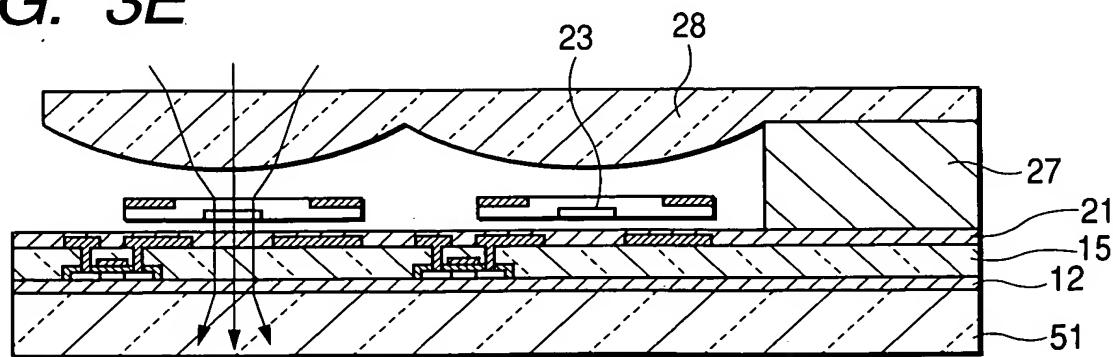


FIG. 4A

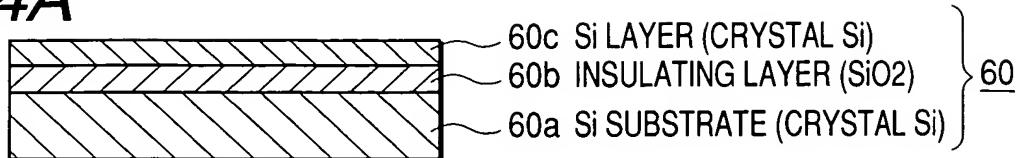


FIG. 4B

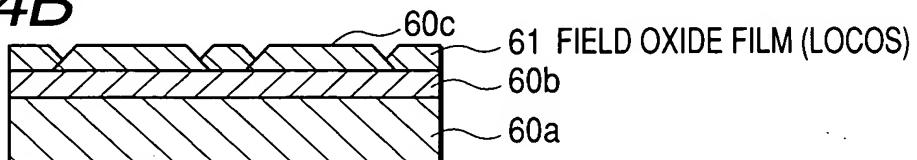


FIG. 4C

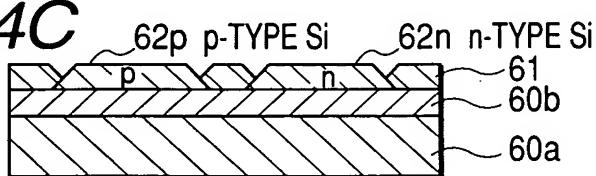


FIG. 4D

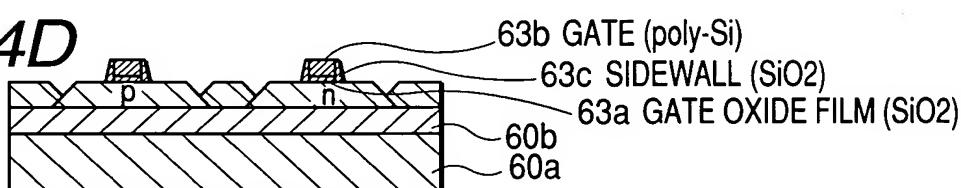


FIG. 4E

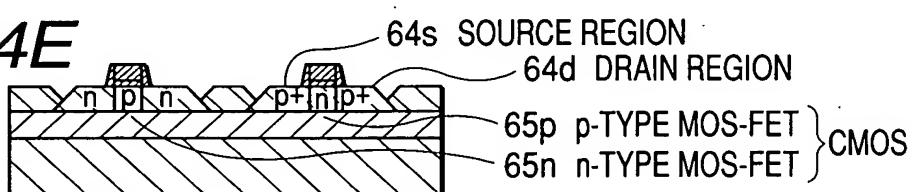


FIG. 4F

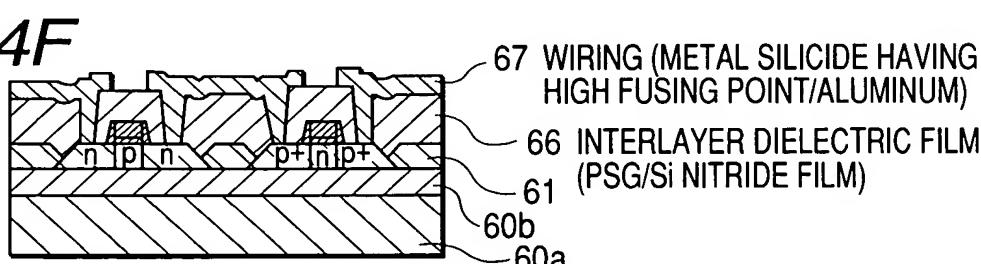


FIG. 4G

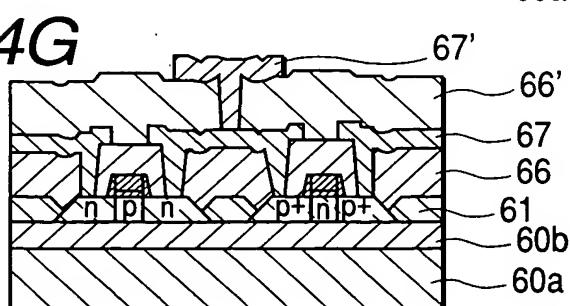


FIG. 4H

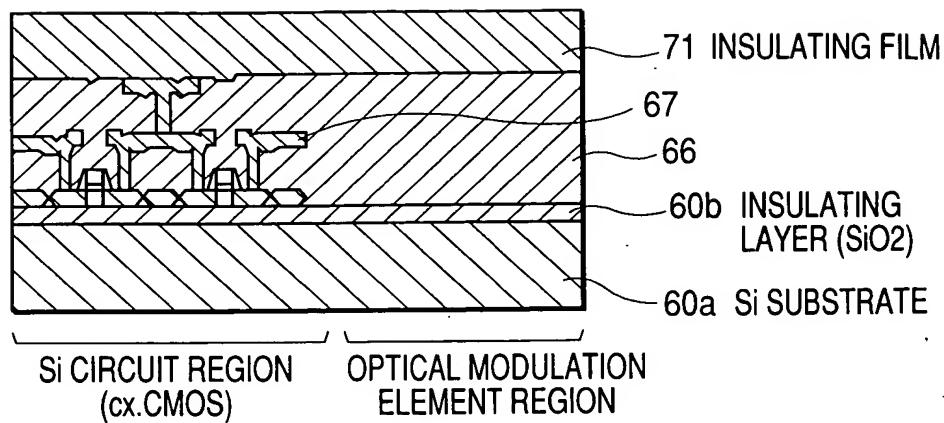
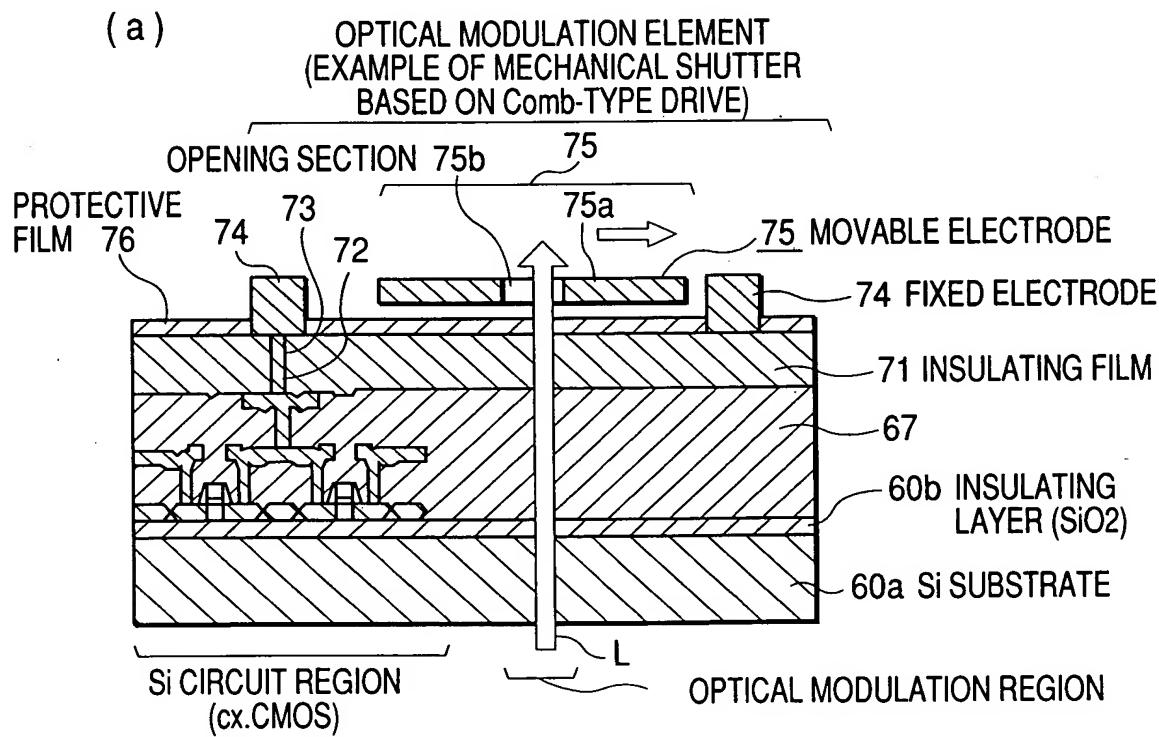


FIG. 4I



(b)

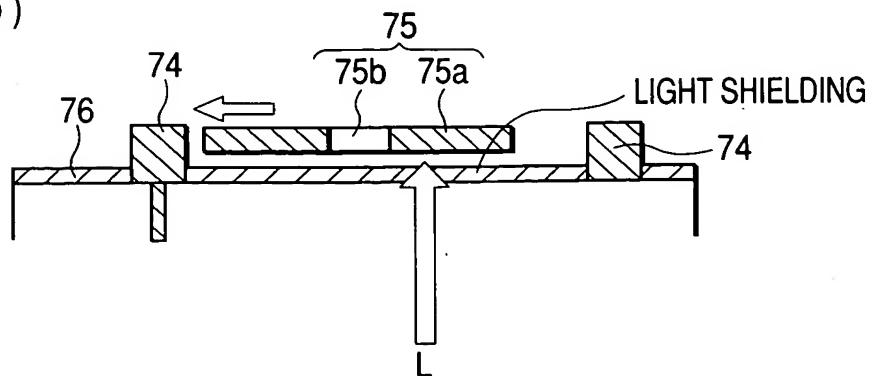


FIG. 5A

A) $V_1 > V_2$

84 SUPPORT SECTION

FIRST FIXED
SECTION 81

82 SECOND
SECTION

85 OPENING
SECTION

84

(1)

FIRST FIXED
SECTION 81

DRIVE CIRCUIT
(ex.CMOS)
885

832 8331 MOVABLE
ELECTRODE

83

(2)

881
TRANSPARENT
SUBSTRATE
(GLASS OR THE LIKE)

886
OPENING
SECTION

887 PLANARIZED INSULATING FILM
884 WIRING CIRCUIT OR LIGHT-SHIELDING FILM
883 INTERLAYER
INSULATING FILM

882 INSULATING LAYER (SiO_2)

FIG. 5B

B) $V_1 = V_2$

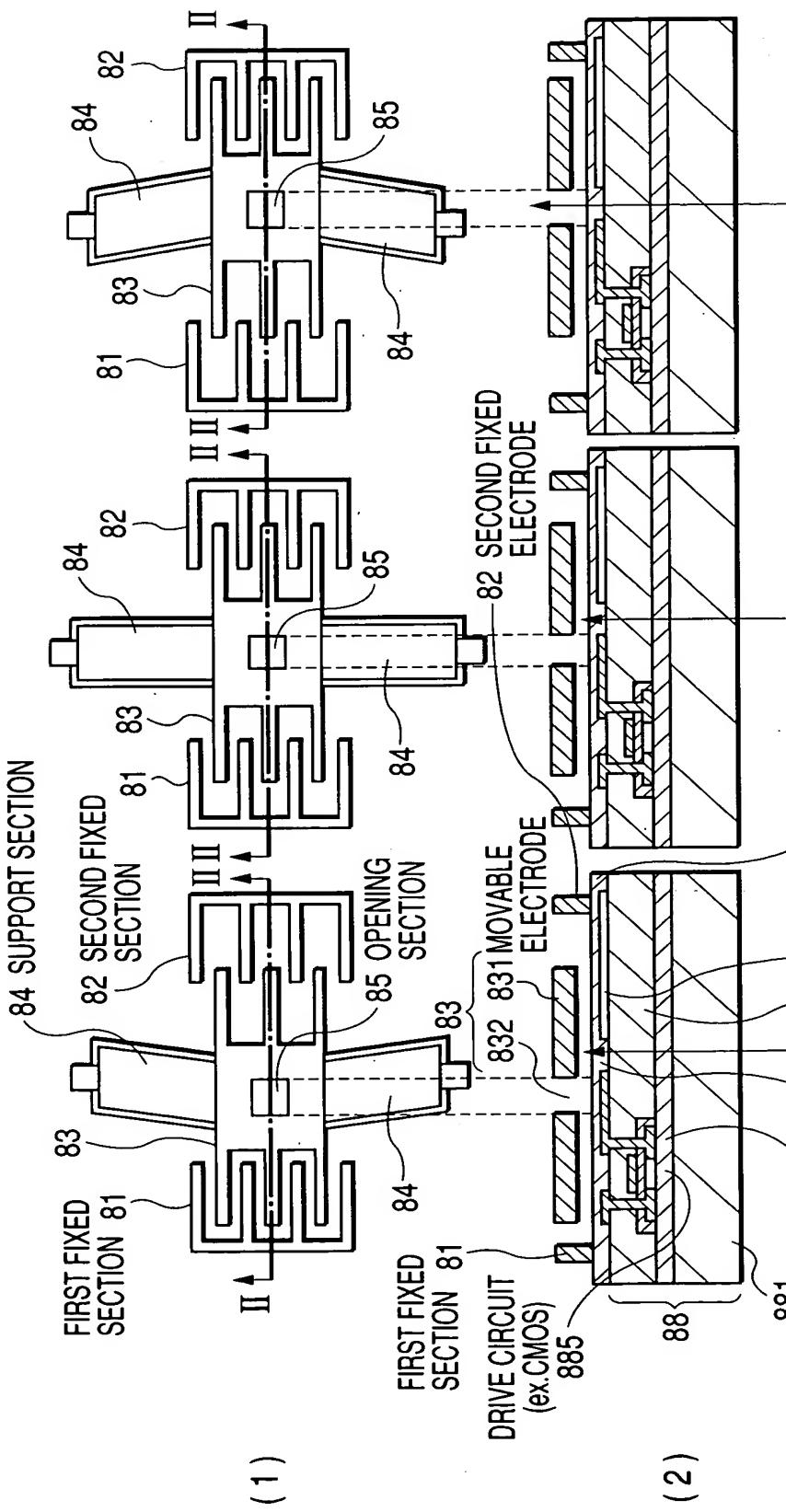
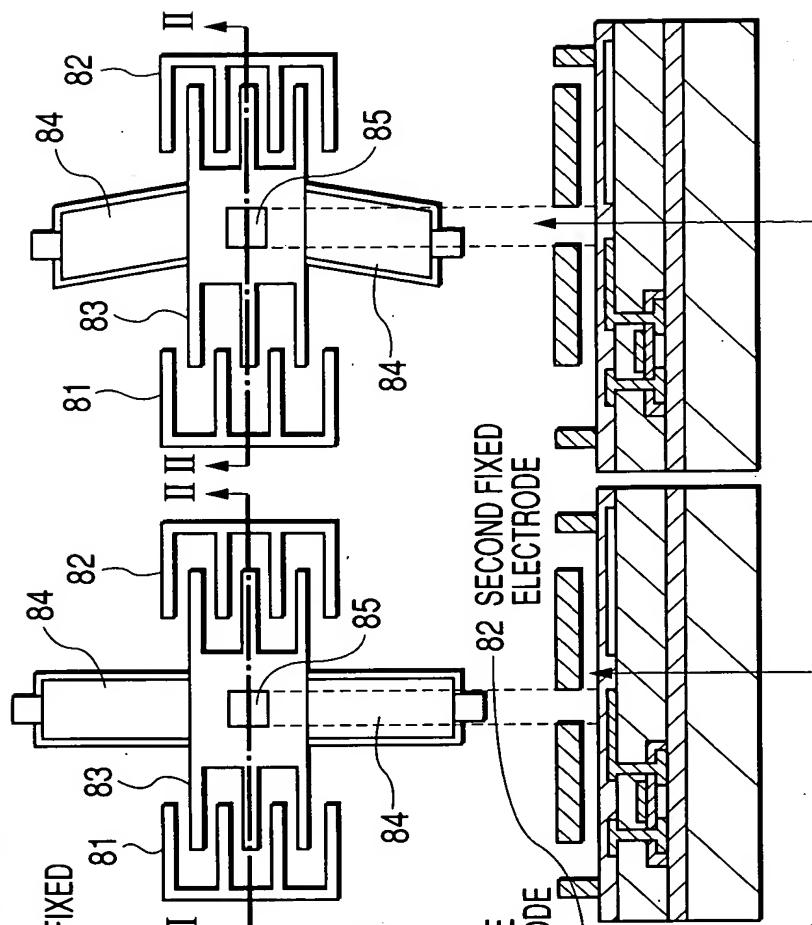


FIG. 5C

C) $V_1 < V_2$



$V_1 =$ POTENTIAL DIFFERENCE BETWEEN FIRST FIXED ELECTRODE AND MOBILE ELECTRODE

$V_2 =$ POTENTIAL DIFFERENCE BETWEEN SECOND FIXED ELECTRODE AND MOBILE ELECTRODE

FIG. 6

